INFORMATION DISCLOSURE STATEMENT	Case Name.	U. Koc 3-1 *10789681*
	Serial No.	
	Applicant:	U. Koc, et al.
	Filing Date:	February 28, 2004
	Group:	

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
HfW	AA	4,509,037	4/2/85	Harris	340	347	12/1/1982

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

HfW	AB	Anderson, R., "Getting the Most out of Delta-Sigma Converters," published online, March 2003, at: www.analogzone.com/acqt0130.pdf ,7 pages.
HfW	AC	Maurino, R., "A 200-MHz IF 11-bit Fourth-Order Bandpass $\Delta\Sigma$ ADC in SiGe," IEEE Journal of Solid-State Circuits, Vol. 35, No. 7, pp. 959-967, July 2000.
HfW	AD	Cherry, J.A., et al., "Continuous-Time Delta-Sigma Modulators for High-Speed A/D Conversion," Kluwer Academic Publishers (2000) pp. vii-ix and 1-29.
HfW	AE	Ritoniemi, T., et al., "Design of Stable High Order 1-Bit Sigma-Delta Modulators," 1990 IEEE Int'l Symposium on Circuits and Systems Vol. 4, New Orleans, LA (May 1-3, 1990), pp. 3267-3270.
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EXAMINER *Howard L. William*	DATE CONSIDERED *16 March 2005*

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

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***References listed beyond AZ would list as AA-1, AB-2, AC-3 thru AZ-26.
***Note First Page ONLY Header/Footer. Subsequent pages must ONLY have page # layout as header